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Fig. 1
(PRIOR ART)

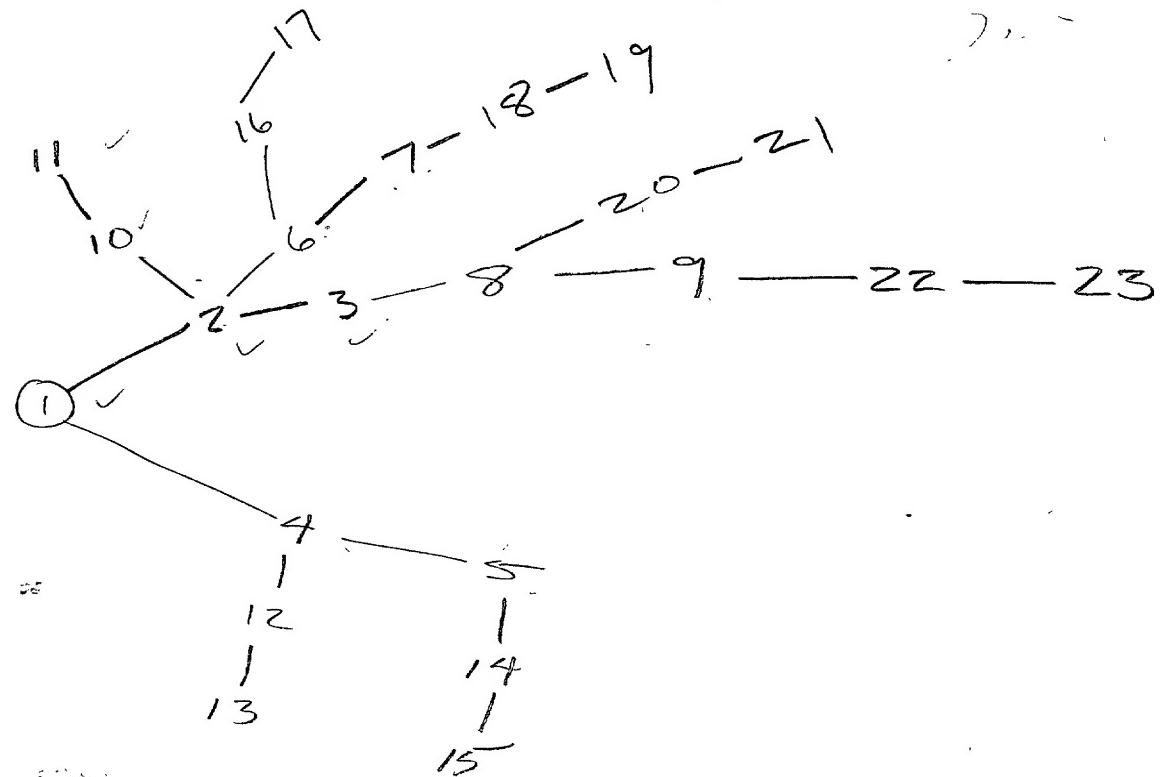
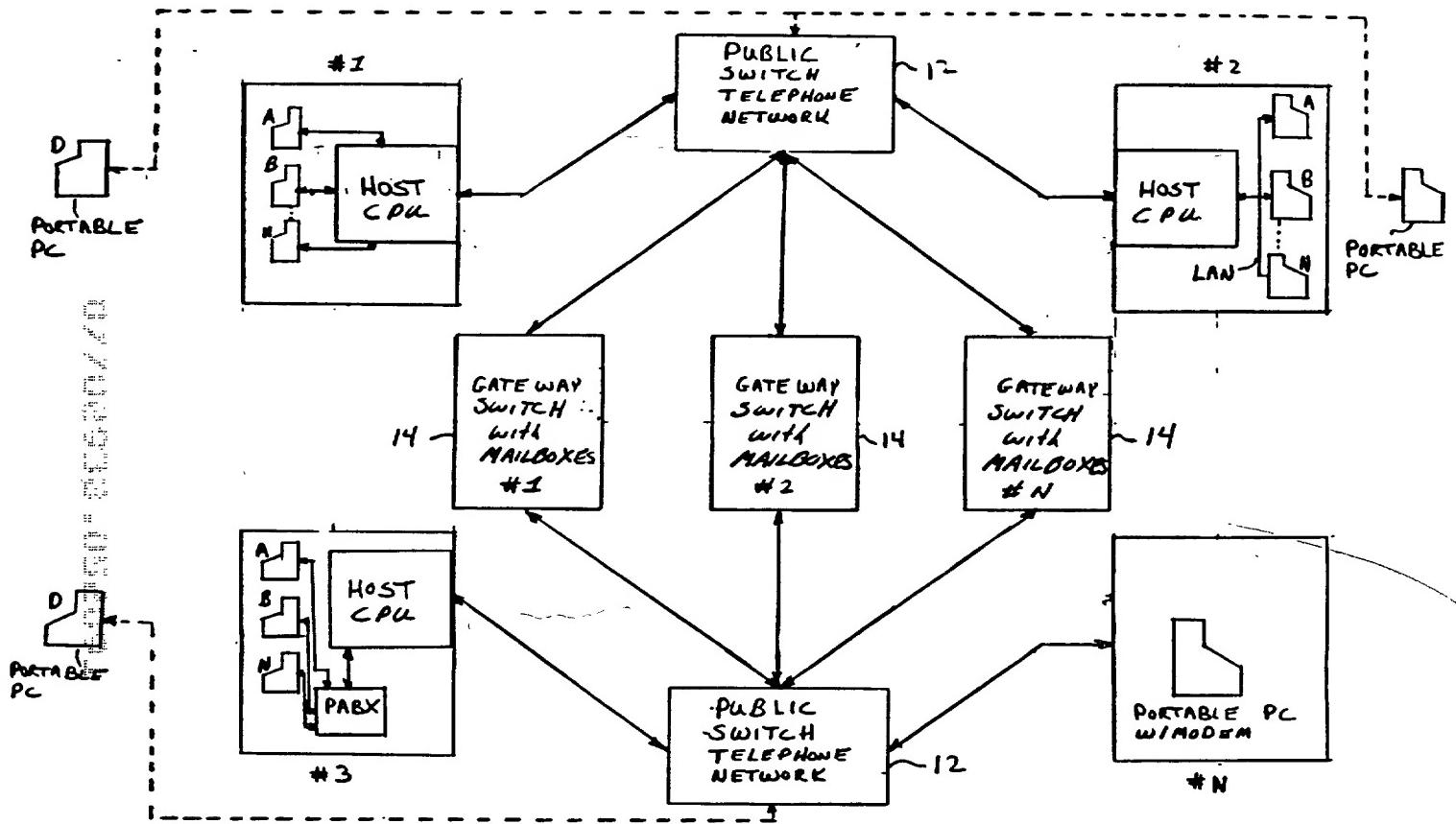
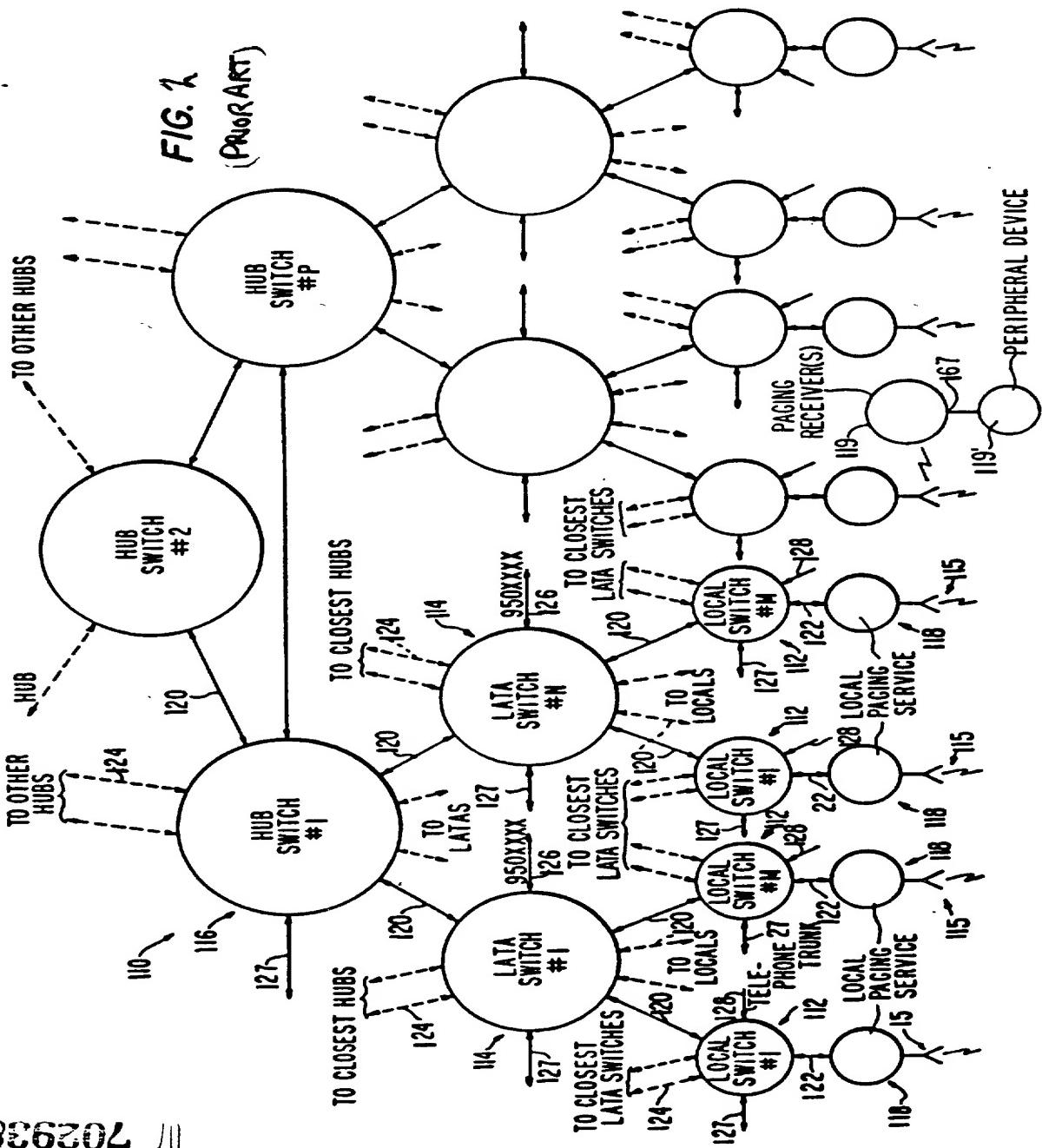


FIG. 1
(Prior Art)



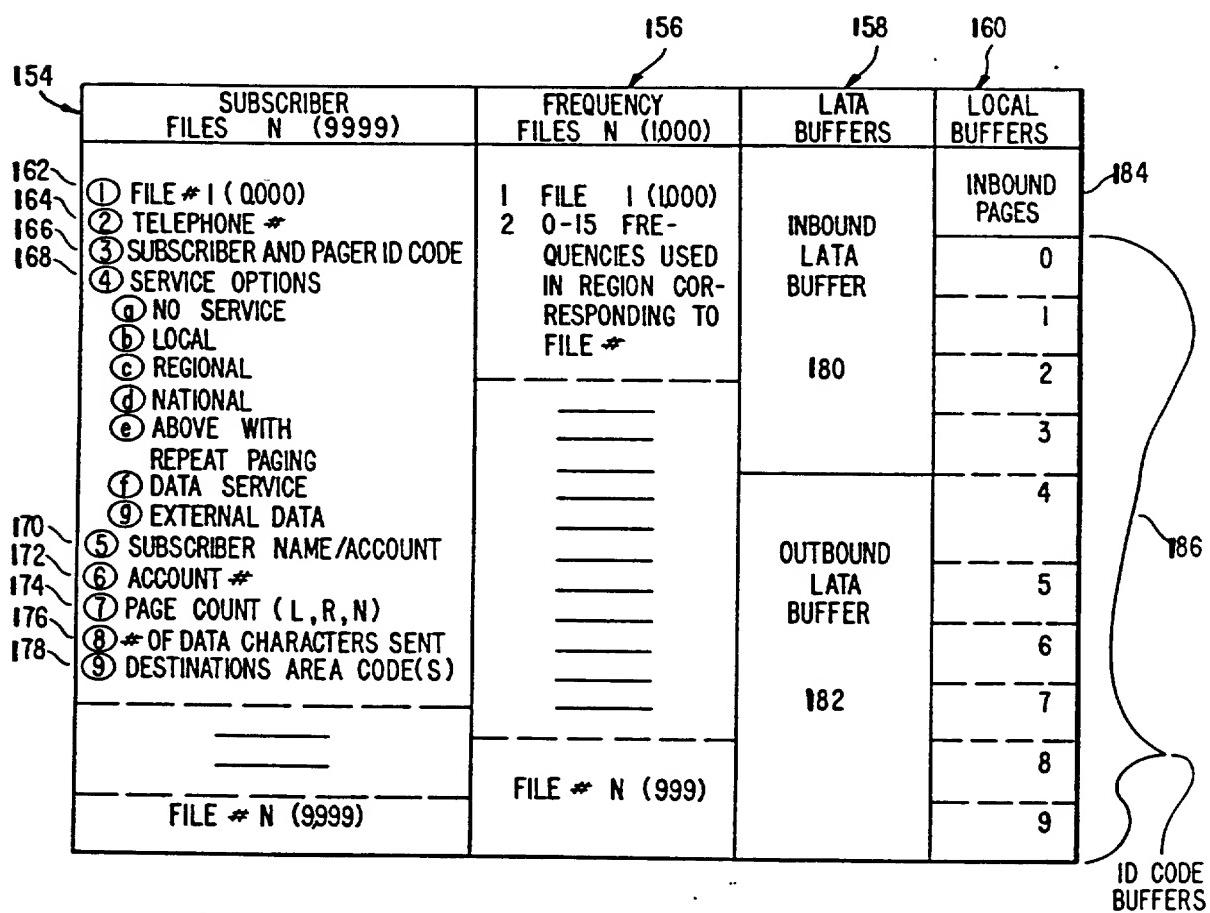
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FIG. 3

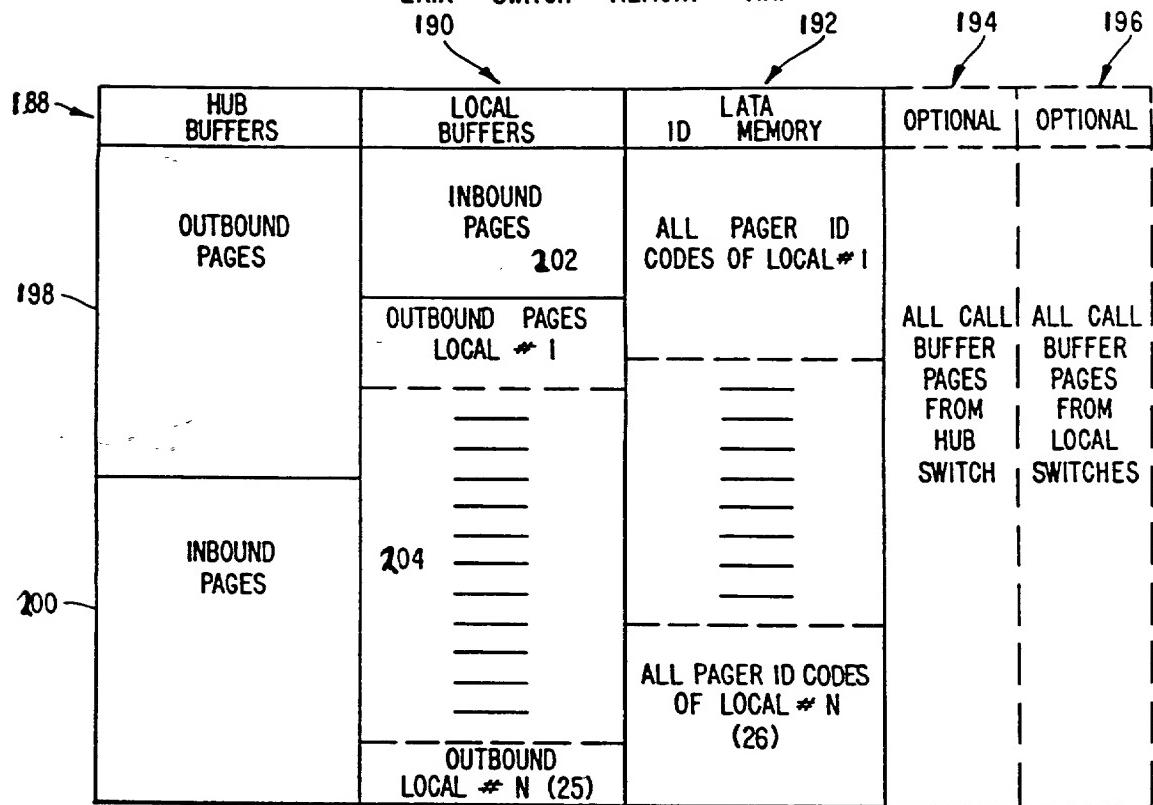
(PRIOR ART)

LOCAL SWITCH MEMORY MAP



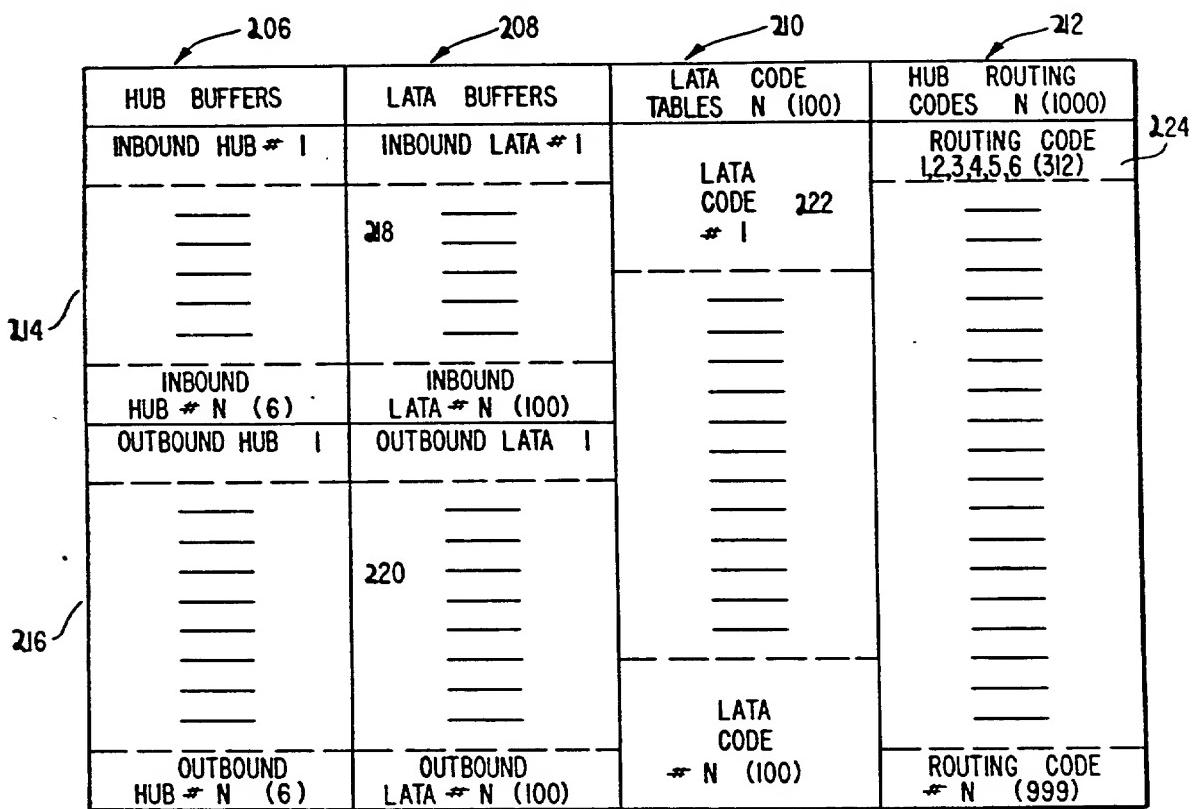
*FIG. 4
(PRIOR ART)*

LATA SWITCH MEMORY MAP



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FIG. 5
(PRIOR ART)
HUB SWITCH MEMORY MAP



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*FIG. 6
(PRIOR ART)*

THE FIVE LAYER MODIFIED X.25 PACKET

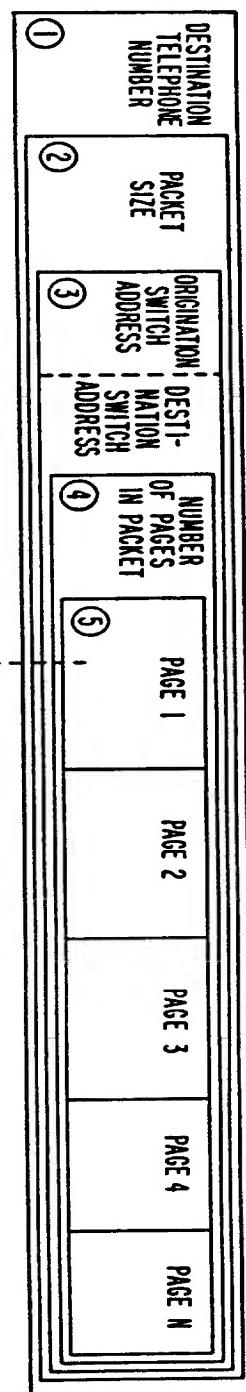
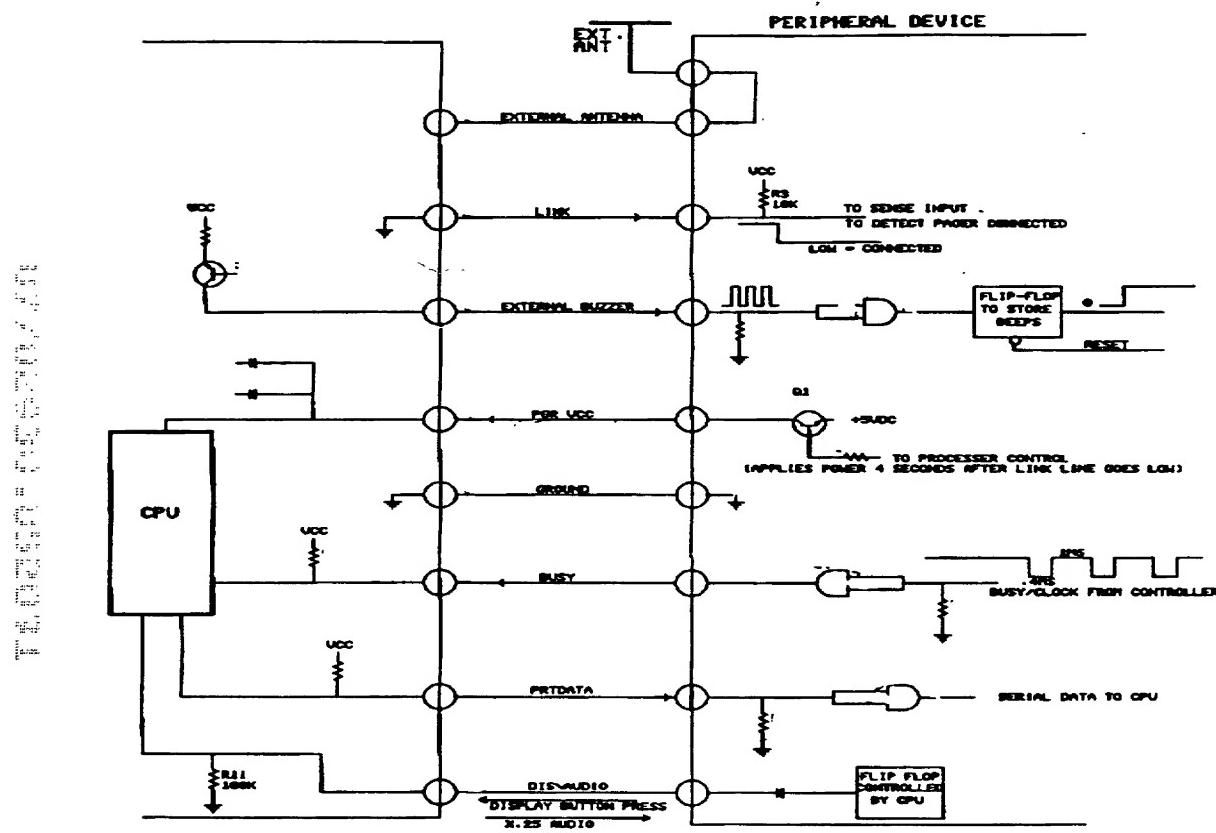


Fig. 7

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(PRIOR ART)



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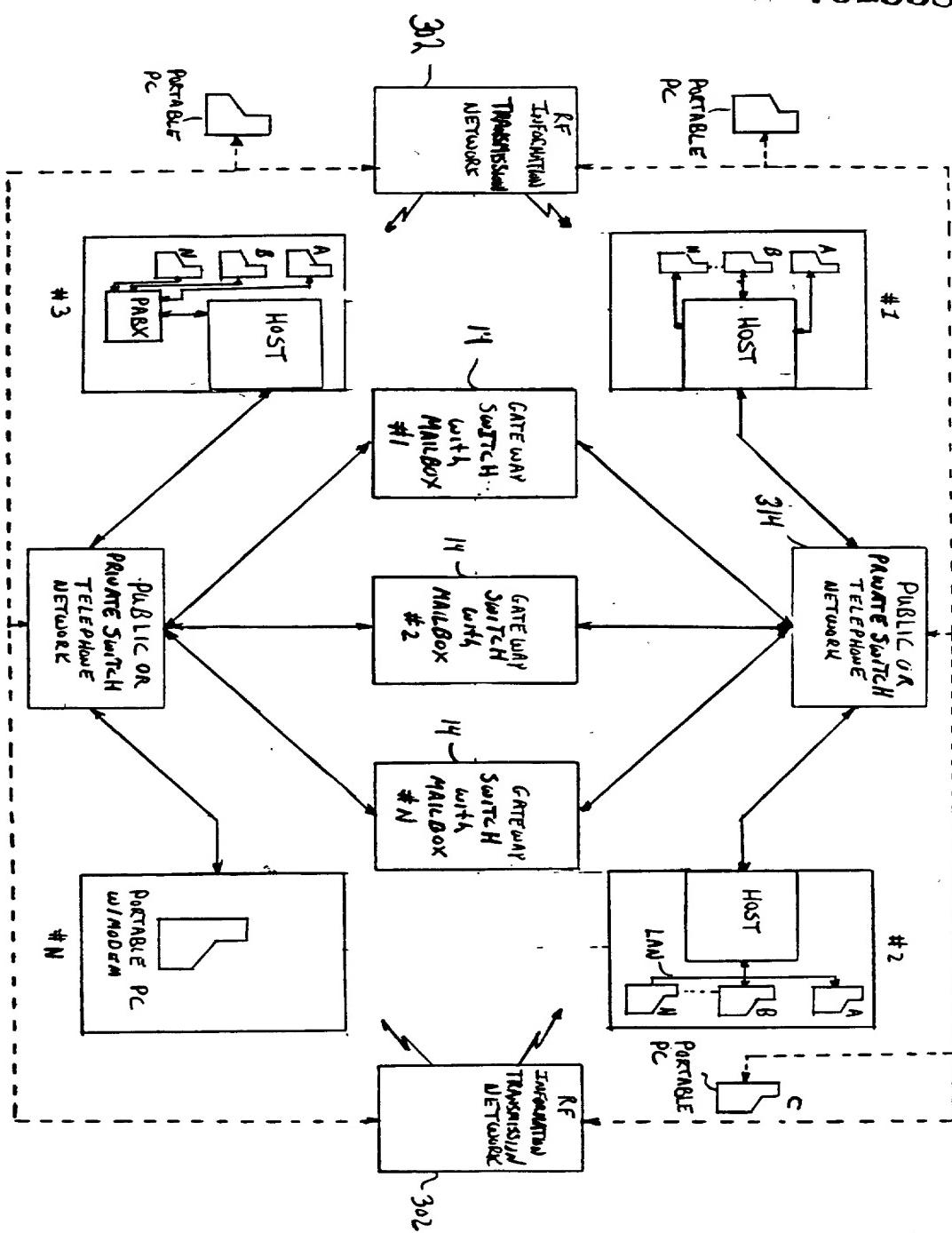
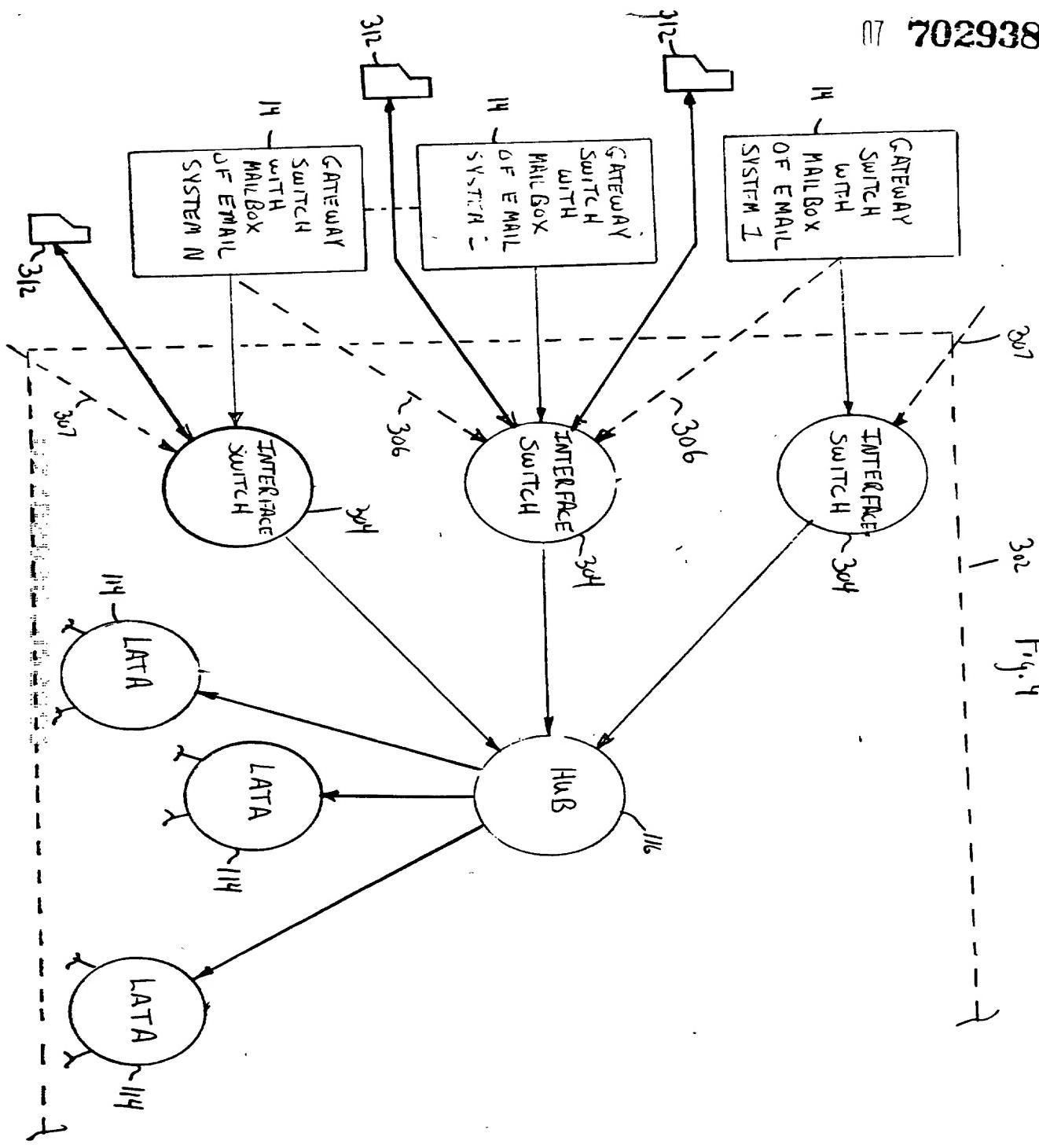


FIG. 8

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Fig. 9

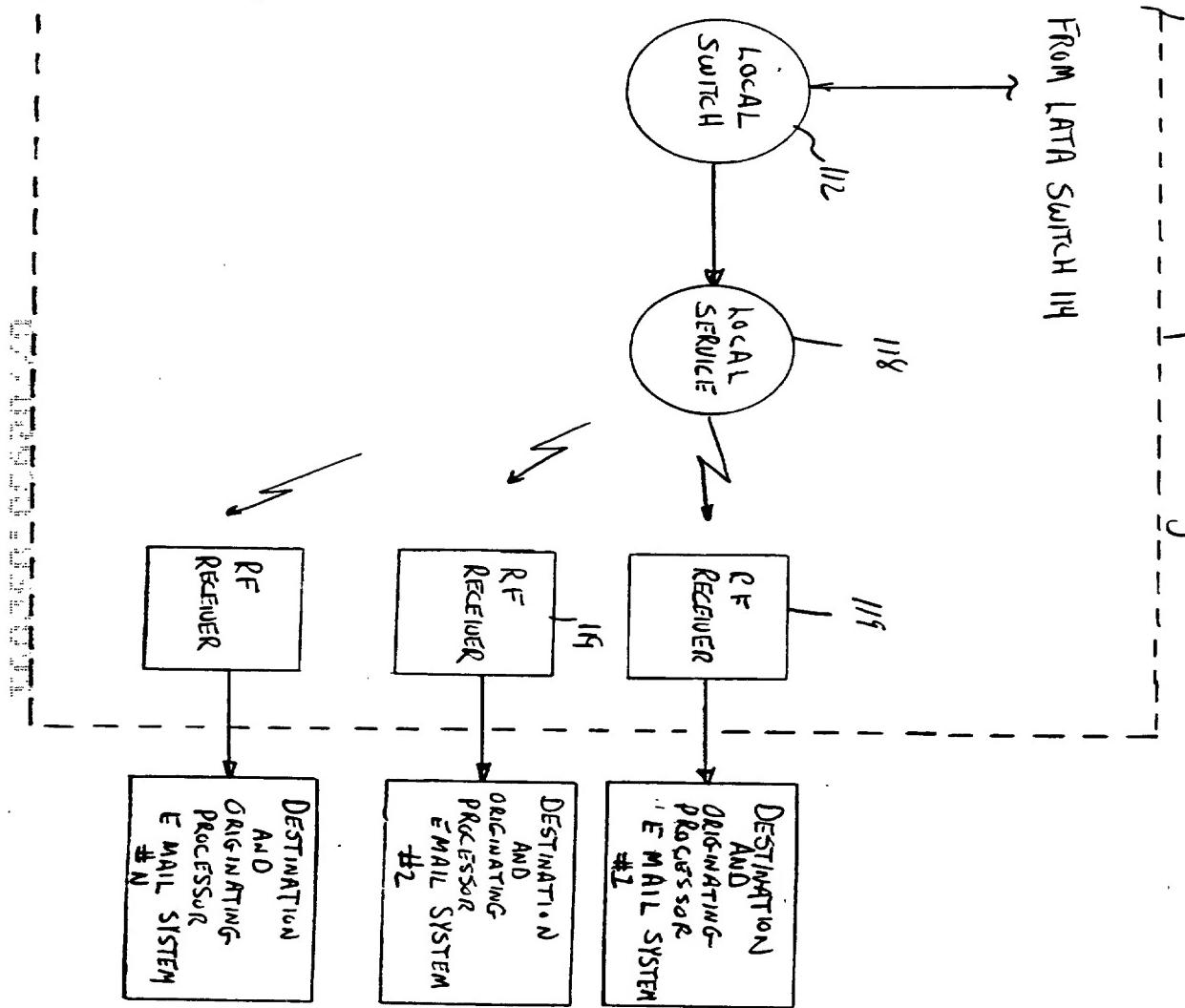


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Fig. 10

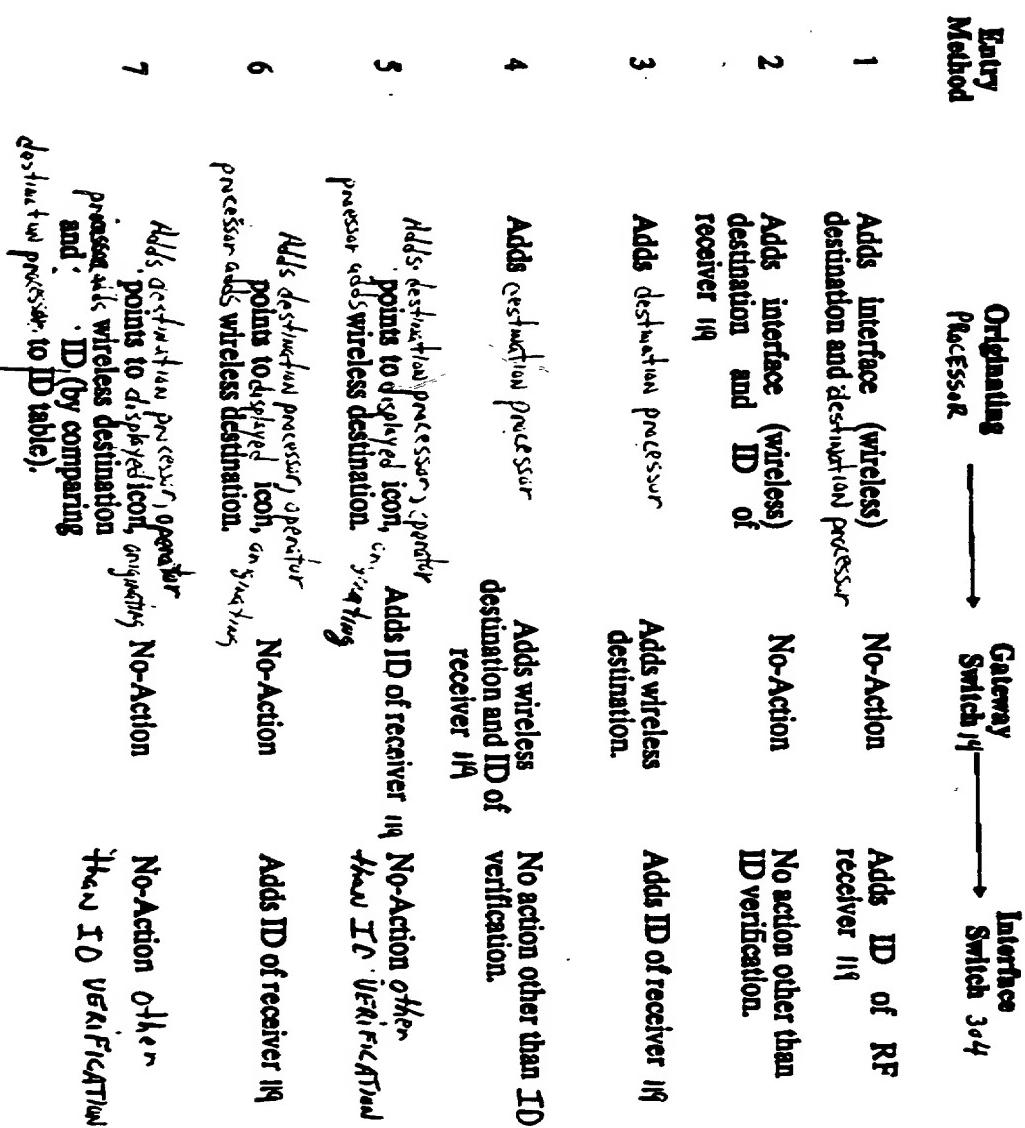
FROM DATA SWITCH 114



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ELECTRONIC MAIL MESSAGE ENTRY METHODS

Fig.11



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